Factsheet

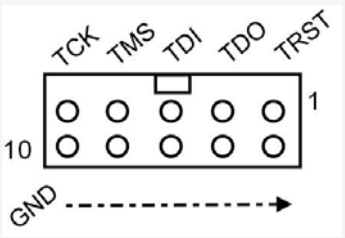
Compact, portable controller for JTAG and boundary-scan applications

Key features

- Compact single TAP (Test Access Port) for flexible board interfacing
- Fully Compatible with IEEE 1149.1, IEEE 1149.6, IEEE 1149.4 and IEEE 1532 standards
- Fully programmable TAP voltage (1.5 V to 3.6 V)
- Up to 6MHz programmable TCK frequency USB Powered (no external PSU needed)
- For boundary-scan /JTAG testing, PLD & FPGA configuration and small-scale flash programming.
- JTAG Live and ProVision compatible
- Stylish contemporary design

Prompted by the steady growth in the use of JTAG/boundary-scan (IEEE Std 1149.1) devices within electronic design, the JTAG Live controller has been developed to offer a truly low-cost, fully functional, JTAG/boundary-scan interface - powered by USB. The market for boundary-scan tools has evolved greatly in recent years and JTAG/boundary-scan logic is now widely fitted as standard to all CPLD's, most microprocessors and many other large pin-count logic devices. The JTAGLive controller offers the opportunity for all engineers to gain experience with this valuable test and debug technology. The JTAGLive controller is compatible with all JTAG Technologies' developer tools such as JTAG ProVision, the free of charge JTAG Live 'Buzz' utility and also JTAG Live Studio.

The controller can be used equally for structural board testing or in-system device programming (ISP) of CPLDs, flash memories etc.. Programmable output levels of 1.5V to 3.6 volts make the JTAGLive controller compatible with all current and future logic families. Test clock frequency may also be programmed from 1KHz to a brisk 6MHz allowing test applications to be executed in a 'snap'. This product represents a significant breakthrough and puts boundary-scan test within reach of many more would-be users.



Recommended pinout